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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 1 of 4

Complete if Known	
Application Number	09/436,618
Filing Date	November 9, 1999
First Named Inventor	Parkes et al.
Group Art Unit	2782
Examiner Name	Unassigned
Attorney Docket Number	200352

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U.S. PATENT DOCUMENTS

Examiner Initials	Doc. No.	U.S. Patent Document: Application or Patent Number	U.S. Patent Document: Kind Code	Name of Patentee or Applicant	Date of Publication	Filing Date If Appropriate

FOREIGN PATENT DOCUMENTS

Examiner Initials	Doc. No.	Foreign Patent Document: Office	Foreign Patent Document: Application or Patent Number	Foreign Patent Document: Kind Code	Name of Patentee or Applicant	Date of Publication	Translation: Yes	Translation: No*+

OTHER - NON PATENT LITERATURE DOCUMENTS

Examiner Initials	Doc. No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number (s), publisher, city and/or country where published.	Translation: Yes	Translation: No*+
	A A	GOSLING, JAMES, et al., "The Java Language Specification," *Addison Wesley*, (1996).		
	A B	AILAMAKI, ANASTASSIA G., et al., "DBMSs On A Modern Processor: Where Does Time Go?," in *the Proceedings of the 25th International Conference on Very Large Data Bases*, (September 1999), pp. 266-277, Edinburgh, Scotland UK		
	A C	NIKHIL, ARVIND and RISHIYUR S., "I-Structure: Data Structures for Parallel Computing," *ACM Transactions on Programming Languages and Systems*, vol. 11(4):598-632, (October 1989).		

Examiner Signature		Date Considered	1/30/03

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 2 of 4

Complete if Known	
Application Number	09/436,618
Filing Date	Novembr 9, 1999
First Named Inventor	Parkes et al.
Group Art Unit	2782
Examiner Name	Unassigned
Attorney Docket Number	200352

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	AD	BANGA, GAURAV, et al., "Scalable Kernel Performance for Internet Servers Under Realistic Loads," in the *Proceedings of the USENIX 1998 Annual Technical Conferenc;* New Orleans, LO, (June 1998), pp.1-12.		
	AE	BLELLOCH, GUY E., et al., "Space-Efficient Scheduling of Parallelism with Synchronization Variables," in *Proceedings of the 9th Annual ACM Symposium on Parallel Algorithms and Architectures (SPAA)*; Newport, Rhode Island, (1997), pp.12-23.		
	AF	CAO, Q., et al., "Detailed Characterization of a Quad Pentium Pro Server Running TPC-D," in *Proceedings of the International Conference on Computer Design (ICCD)*; Austin, TX, (October 1999), pp. 108-115,		
	AG	CHILIMBI, TRISHUL, M., et al., "Cache-Conscious Structure Definition," in the *Proceedings of the ACM SIGPLAN '99 Conference on Programming Language Design and Implementation (PLDI)*; Atlanta, GA, (May 1999), , pp. 13-24.		
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	AI	EICKEMEYER, RICHARD, J., et al., "Evaluation of Multithreaded Uniprocessors for Commercial Application Environments," in *Proceedings of the 23rd Annual International Symposium on Computer Architecture*; Philadelphia, PA, (May 1996), pp. 203-212,.		
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	AK	HAREL, D., et al., "On the Development of Reactive Systems," in *Logics and Models of Concurrent Systems*, vol. F13, K.R. Apt, Ed. Berlin: Springer-Verlag (1985), pp. 477-498.		
	AL	JAYASIMHA, JAY, et al., "Thread-based Cache Analysis of a Modified TPC-C Workload," *in Proceedings of the Second Workshop on Computer Architecture Evaluation Using Commercial Workload*; Orlando, FL. (1999).		
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	AN	LAMARCA, ANTHONY, et al., "The Influence of Caches on the Performance of Heaps," *The ACM Journal of Experimental Algorithmics*, (1997), pp.1-32.		
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	AP	MALTZAHN, CARLOS, et al., "Performance Issues of Enterprise Level Web Proxies," in Proceedings of the *1997 ACM Sigmetrics International Conference on Measurement and Modeling of Computer Systems*; Seattle, WA, (June 1997), pp.13-23.		
	AQ	MORRIS, ROBERT, et al., "The Click Modular Router," in *Proceedings of the 17th ACM Symposium on Operating Systems Principles (SOSP '99)*; Charleston, South Carolina, vol. 33(5):217-231 (December 1999).		

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 3 of 4

Complete if Known	
Application Number	09/436,618
Filing Date	November 9, 1999
First Named Inventor	Parkes et al.
Group Art Unit	2782
Examiner Name	Unassigned
Attorney Docket Number	200352

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OTHER - NON PATENT LITERATURE DOCUMENTS				
Examiner Initials	Doc. No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number (s), publisher, city and/or country where published.	Translation Yes	Translation No*+
	AR	PAI, VIVEK, S., et al., "Flash: An efficient and portable Web Server," in *Proceedings of the 1999 USENIX Annual Technical Conference*; Monterey, CA, (June 1999), pp.199-212.		
	AS	PETTIS, KARL, et al., "Profile Guided Code Positioning," in *Proceedings of the ACM SIGPLAN '90 Conference on Programming Language Design and Implementation*; White Plans, NY., (June 1990), SIGPLAN NOTICES, Vol. 25(6):16-27.		
	AT	PHILBIN, JAMES, et al., "Thread Scheduling for Cache Locality," in *Proceedings of the 7th International Conference on Architectural Support for Programming Languages and Operating systems (ASPLOS VII)*; Cambridge, MA, (October 1996), pp. 60-71.		
	AU	RANGANATHAN, PARTHASARATHY, et al., "Performance of Database Workloads on Shared-Memory Systems with Out-of-Order Performance," in the *Proceedings of the 8th International Conference on Architectural Support for Programming Languages and Operating Systems (ASPLOS VII)*; San Jose, CA., (October 1998), pp.307-318.		
	AV	TORRELLAS, JOSEP, et al., "Optimizing the Instruction Cache Performance of the Operating System," *IEEE Transactions on Computers*. Vol. 47(12):1363-1381, (December 1998).		
	AW	WAND, MITCHELL, "Continuation-Based Multiprocessing," in *Proceedings of the 1980 LISP Conference*; Stanford University, Palo Alto, CA, (August 1980), pp. 19-28.		
	AX	HILL, MARK, D., et al., "Evaluating Associativity in CPU Caches," *IEEE Transactions on Computers*; Vol. 38(12):1612-1630, (1989).		
	AY	LEE, EDWARD, A., et al., "Dataflow Process Network," *Proceedings of the IEEE*; Vol. 83(5):773-799, (May 1995).		
	AZ	MOGUL, JEFFREY, C., et al., "The Effect of Context Switches on Cache Performance," in Proceedings of the Fourth International Conference on Architectural Support for Programming Languages and Operating Systems," Santa Clara, CA, (April 1991), pp. 75-85.		
	BA	NAJJAR, WALID A., et al., "Advances in the dataflow computational Model," Parallel Computing, vol. 25, (1999).		
	BB	PERL, SHARON, et al., "Studies of Windows NT Performance Using Dynamic Execution Traces," in *Proceedings of the 2nd USENIX Symposium on Operating Systems Design and Implementation (OSDI)*; Vol. 30, Special Issue (1996), Seattle, WA. (October 1997).		
	BC	ROSENBLUM, MENDEL, et al., "The Impact of Architectural Trends on Operating System Performance," *Copper Mountain Resort*, CO., (December 1995), pp. 285-298.		
	BD	ACAR, UMUT, A., et al., "The Data Locality of Work Stealing," in *Proceedings of the Twelfth Annual ACM Symposium on Parallel Algorithms and Architectures (SPAA 2000)*; Bar Harbor, ME., (July 2000).		
	BE	ANDERSON, THOMAS E., "The Performance of Spin Lock Alternatives for Shared-Memory Multiprocessors,"*IEEE Transaction on Parallel And Distributed Systems*," vol. 1(1):6-16, (1990).		
	BF	BANGA, GAURAV, et al., "Better Operating System Features for Faster Network Servers," in the *Proceedings of the Workshop on Internet Server Performance*; Madison, WI June 1998.		
	BG	BARROSO, LUIZ, ANDRE, et al., "Memory System Characterization of Commercial Workloads," in *Proceedings of The 25th Annual International Symposium on Computer Architecture*; Barcelona, Spain, (June 27-July 1, 1998), pp. 3-14.		
Examiner Signature			Date Considered	1/23/03

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 4 of 4

Complete if Known	
Application Number	09/436,618
Filing Date	November 9, 1999
First Named Inventor	Parkes et al.
Group Art Unit	2782
Examiner Name	Unassigned
Attorney Docket Number	200352

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OTHER - NON PATENT LITERATURE DOCUMENTS

Examiner Initials	Doc. No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number (s), publisher, city and/or country where published.	Translation Yes	Translation No*+
	B H	BLACKWELL, TREVOR, "Speeding up Protocols for Small Messages," in *Proceedings of ACM SIGCOMM '96 Conference on Applications, Technologies, Architectures, and Protocols for Computer Communications*; Palo Alto, CA, (August 1996), pp. 85-95.		
	B I	BLUMOFE, ROBERT D., et al., "Cilk: An Efficient Multithreaded Runtime System," *Journal of Parallel and Distributed Computing*, vol. 37(1):55-69, (1996).		
	B J	CHANKHUNTHOD, ANAWAT, et al., "A Hierarachical Internet Object Cache," in *Proceedings of the USENIX 1996 Annual Technical Conference*; San Diego, CA., (January 1996), pp. 153-163.		
	B K	DRAVES, RICHARD, P., et al., "Using Continuations to Implement Thread Management and Communication in Operating Systems," in *Proceedings of the Thirteenth ACM Symposium on Operating Systems Principles*; Pacific Grove, CA., (October 1991), pp. 122-136.		
	B L	PERL, SHARON, E., et al., "Studies of Windows NT Performance Using Dynamic Execution Traces," in *Proceedings of the Second Symposium on Operating Systems Design and Implementation (OSDI '96)*; Seattle, WA, (October 1996), pp.169-183.		
	B M	CHANDRA, SANDRA, et al., "Teapot: A Domain-Specific Language for Writing Cache Coherence Protocols," *IEEE Transactions on Software Engineering*; vol. 25(3):pp317-333, (1999).		
	B N	CHEN, J. BRADLEY, "Memory Behavior of an X11 Window System," in the *Proceedings of the Winter 1994 USENIX Conference*; San Francisco, CA, (January 1994), pp.189-200.		
	B O	PAPADIMITRIOU, CHRISTOS, "The Theory of Database Concurrency Control," Rockville, MD., Computer Science Press, (1986) pp. 1-234.		
	B P	GRAY, JIM, "The Benchmark Handbook for Database and Transaction Processing Systems," Second Edition, Morgan Kaufmann Publishers, Inc., San Mateo, CA. (1993), pp. 1-586.		
	B Q	AGHA, Gul A., "ACTORS: A Model of Concurrent Computation in Distributed Systems", Cambridge, CA: *MIT Press*, (1988)		
	B R	ACKERMAM William B., et al., "VAL-A Value –Oriented Algorithmic Language: Preliminary Reference Manual," MIT Laboratory for Computer Science, Cambridge, MA MIT/LCS/TR-218, (June 1979)		
	B S	PATTERSON, David, A., et al., "Computer Architecture: A Quantitative Approach, 2ed.. Morgan Kaufmann (1996), Chapters 3, 4 and 5.		

Examiner Signature		Date Considered	1/30/07

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